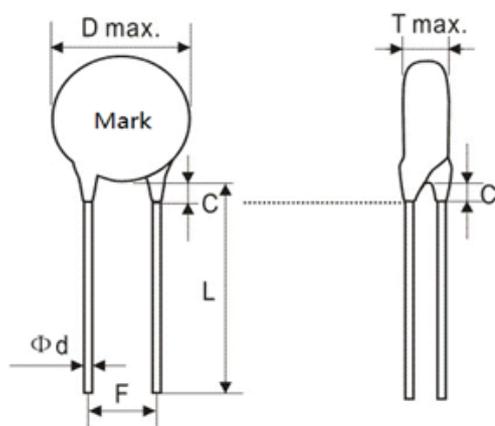


■ Outline Drawing



■ Typical Applications

Line-to-line (class X) filtering  
 Line-to-ground (class Y) filtering, Antenna coupling  
 Primary and secondary coupling (switching power supplies) and line disturbances suppression (motors and motor controls, relays, switching power supplies and invertors).

■ Features

Safety Standard Recognized IEC60384-14  
 Reliable operation up to 125°C  
 Capacitance offerings ranging from 10~4,700 pF  
 Lead (Pb)-free and RoHS Compliant, Halogen free  
 High reliability  
 Encapsulation meets flammability standard UL 94V-0

■ Safety Approvals

UL	UL60384-14 and E60384-14	E302125	
ENEC/VDE	IEC 60384-14	40050259	
CQC	IEC 60384-14:2005	CQC19001219045	

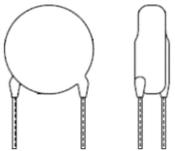
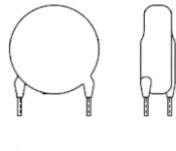
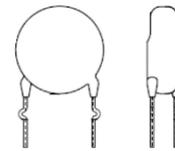
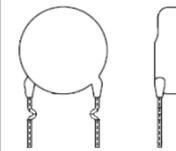
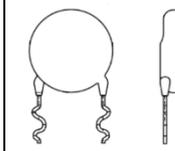
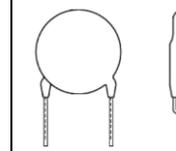
■ Specifications

Dielectric / Temperature Characteristic	Y5P	Y5U	Y5V
Capacitance Change with Reference to +25°C and 0 VDC Applied	±10%	+22%, -56%	+22, -82%
Operating Temperature Range	-25°C~125°C		
Rated Voltage	X1 440Vac , Y1 400Vac		
Test Voltage Between Terminals	4,000 VAC, 50 Hz, 60 seconds		
Insulation Resistance	> 6000MΩ		
Dissipation Factor (tanδ) at 1KHz and 25°C	2.5% Max	2.5% Max	2.5% Max

■ Product code system

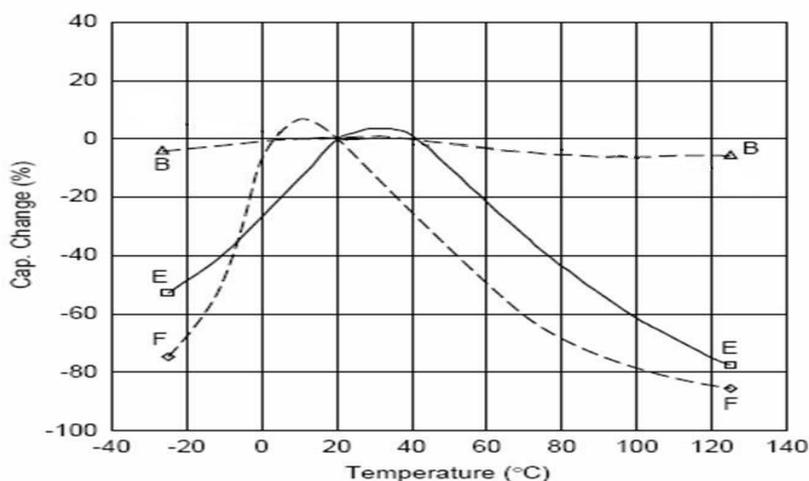
PY1	F	A	102	M	A	400	A	B	10	23
Type	Temperature characteristics	Internal use	Nominal capacity	Tolerance	Internal use	Rated Voltage	Voltage	Lead forming	Lead Pitch	Lead length
PY1= X1/Y1	B=Y5P E=Y5U F=Y5V	--	4R7=4.7pF 220=22pF 471=470pF	K=±10% M=±20%	--	400= 400Vac	A=AC	Shown as Table I	05=5mm 08=7.5mm 10=10mm 13=12.5mm	04= 3.5mm 23= 23mm

Table I

Code	B	K	R	U	W	S
Lead Forming						

■ Temperature Characteristic Curves

B: (Y5P) E: (Y5U) F: (Y5V)



Code	Lower temperature	Limit temperature	Reference temperature	The biggest electric capacity relative change rate
Y5P (B)	-30°C	+85°C	+25°C	±10%
Y5U (E)	-30°C	+85°C	+25°C	+22%, -56%
Y5V (F)	-30°C	+85°C	+25°C	+22, -82%

■ Dimensions (mm)

Dielectric/Temp. Char.	Part number	Capacitance	Tolerance	D Max	T Max	F±0.5	ø d± 0.05
± 10% (Y5P)	PY1B_100K_400A*10**	10 pF	±10%	8	6	10	0.55
	PY1B_180K_400A*10**	18 pF		8			
	PY1B_220K_400A*10**	22 pF		8			
	PY1B_330K_400A*10**	33 pF		8			
	PY1B_470K_400A*10**	47 pF		8			
	PY1B_560K_400A*10**	56 pF		8			
	PY1B_680K_400A*10**	68 pF		7.3			
	PY1B_101K_400A*10**	100 pF		7			
	PY1B_151K_400A*10**	150 pF		7			
	PY1B_221K_400A*10**	220 pF		7			
	PY1B_331K_400A*10**	330 pF		8			
	PY1B_471K_400A*10**	470 pF		9			
	PY1B_681K_400A*10**	680 pF		9.8			
+22 ~ -56% (Y5U)	PY1E_331K_400A*10**	330 pF	±10%	6.8	6	10	0.55
	PY1E_471K_400A*10**	470 pF		6.8			
	PY1E_681K_400A*10**	680 pF		8			
	PY1E_102M_400A*10**	1000 pF	±20%	8			
	PY1E_152M_400A*10**	1500 pF		9.8			
	PY1E_222M_400A*10**	2200 pF		10.7			
	PY1E_332M_400A*10**	3300 pF		13.5			
	PY1E_472M_400A*10**	4700 pF		13.5			
+22 ~ -82% (Y5V)	PY1F_102M_400A*10**	1000 pF	±20%	6.8	6	10	0.55
	PY1F_152M_400A*10**	1500 pF		8			
	PY1F_222M_400A*10**	2200 pF		9			
	PY1F_332M_400A*10**	3300 pF		10.6			
	PY1F_472M_400A*10**	4700 pF		12			

\_ = Internal use

\* = Lead forming

\*\* = Lead length

■ Marking (Example)

	Trademark	 WDC			
	Type	CD	X1/Y1	CE	X1/Y2
	capacitance	472 (4700pF)			
	Tolerance	K (±10%) · M (±20%)			
	Safety recognized				
	Rated Voltage	X1 440 ~ (440Vac) Y1 400 ~ (400Vac)			

■ Safety Approvals

Approval marks	Capacitor Class	Certificate	Climatic Category	Rated Cap.	Rated Voltage	
					Y1	Y2
	X1/Y1 (CD)	E302125	25/125/21/B	10 ~ 4700 pF	X1:440Vac Y1:400Vac	X1:400Vac Y2:300Vac
	X1/Y2 (CE)			100 ~ 10000 pF		
 	X1/Y1 (CD)	40050259	25/125/21/B	10 ~ 4700 pF		
	X1/Y2 (CE)	40050253		100 ~ 10000 pF		
	X1/Y1 (CD)	CQC19001219045	25/125/21/B	10 ~ 4700 pF		
	X1/Y2 (CE)	CQC19001219046		100 ~ 10000 pF		

■ Specifications and Test Methods

Test condition :

Test and measurement shall be made at the standard condition

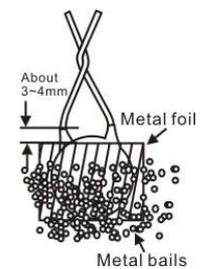
(Temperature 15 ~ 35°C, relative humidity 45 ~ 75% and atmospheric pressure 86 ~ 106Kpa).

Unless otherwise specified herein. If doubt occurred on the value of measurement, and measurement was requested by customer capacitors shall be measured at the reference condition

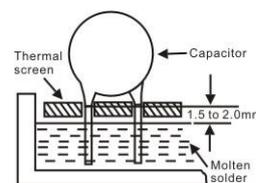
(Temperature 25±2°C, relative humidity 60 ~ 70% and atmospheric pressure 86~106Kpa.)

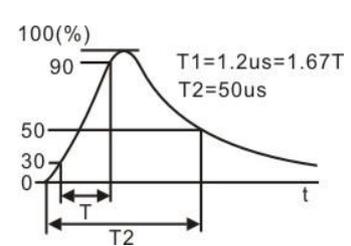
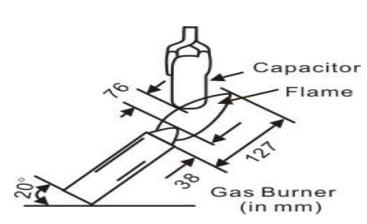
Performance (Apply to Class X1/Y1 & X1/Y2)

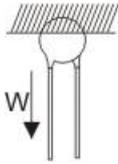
Test items	Performance	Test Method
Climatic category	25/125/21/B	--
Appearance and Dimensions	No marked defect on appearance form and dimensions are within specified range.	The capacitor should be visually inspected for evidence of defect.
Marking	To be easily legible	The capacitor should be visually inspected.
Capacitance	Within specified tolerance	The capacitance and dissipation factor should be measured at 25°C with 1±0.1KHz and AC1.0V (r.m.s.)
Dissipation Factor (D.F.)	D.F. ≤ 2.5%	
Insulation Resistance (I.R.)	>6000MΩ	The insulation resistance should be measured with DC100V within 60±5 sec of charging.

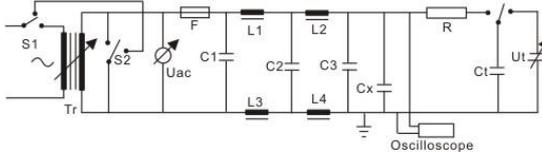
Item		Specification	Testing Method																			
Dielectric Strength	Between Lead Wires	No failure.	<p>The capacitor should not be damaged when test voltages of Table 1 are applied between the lead wires for 60 sec. (Charge/Discharge current <math>\leq 50\text{mA}</math>)</p> <p style="text-align: center;">&lt; Table 1 &gt;</p> <table border="1"> <thead> <tr> <th>Type</th> <th>Test Voltage</th> </tr> </thead> <tbody> <tr> <td>X1Y2</td> <td>AC1500V (r.m.s.)</td> </tr> <tr> <td>X1Y1</td> <td>AC4000V (r.m.s.)</td> </tr> </tbody> </table>	Type	Test Voltage	X1Y2	AC1500V (r.m.s.)	X1Y1	AC4000V (r.m.s.)													
	Type	Test Voltage																				
X1Y2	AC1500V (r.m.s.)																					
X1Y1	AC4000V (r.m.s.)																					
Body Insulation	No failure.	<p>First, the terminals of the capacitor should be connected together. Then, as shown in figure at right, a metal foil should be closely wrapped around the body of the capacitor to the distance of about 3 to 4mm from each terminal.</p>  <p>Then, the capacitor should be inserted into a container filled with metal balls of about 1mm diameter. Finally, AC voltage of Table 2 is applied for 60 sec. between the capacitor lead wires and metal balls.</p> <p style="text-align: center;">&lt; Table 2 &gt;</p> <table border="1"> <thead> <tr> <th>Type</th> <th>Test Voltage</th> </tr> </thead> <tbody> <tr> <td>X1/Y2</td> <td>AC1500V(r.m.s.)</td> </tr> <tr> <td>X1/Y1</td> <td>AC4000V(r.m.s.)</td> </tr> </tbody> </table>	Type	Test Voltage	X1/Y2	AC1500V(r.m.s.)	X1/Y1	AC4000V(r.m.s.)														
Type	Test Voltage																					
X1/Y2	AC1500V(r.m.s.)																					
X1/Y1	AC4000V(r.m.s.)																					
Temperature Characteristics	<table border="1"> <thead> <tr> <th>Char.</th> <th>Capacitance Change</th> </tr> </thead> <tbody> <tr> <td>B (Y5P)</td> <td><math>\pm 10\%</math></td> </tr> <tr> <td>E (Y5U)</td> <td>+22/-56%</td> </tr> <tr> <td>F (Y5V)</td> <td>+22/-82%</td> </tr> </tbody> </table> <p>Temp. range: -25 ~ +85°C</p>	Char.	Capacitance Change	B (Y5P)	$\pm 10\%$	E (Y5U)	+22/-56%	F (Y5V)	+22/-82%	<p>The capacitance measurement should be made at each step specified in Table 3.</p> <p style="text-align: center;">&lt;Table 3&gt;</p> <table border="1"> <thead> <tr> <th>Step</th> <th>1</th> <th>2</th> <th>3</th> <th>4</th> <th>5</th> </tr> </thead> <tbody> <tr> <td>Temperature (°C)</td> <td>25 <math>\pm 2</math></td> <td>-25 <math>\pm 2</math></td> <td>25 <math>\pm 2</math></td> <td>85 <math>\pm 2</math></td> <td>25 <math>\pm 2</math></td> </tr> </tbody> </table>	Step	1	2	3	4	5	Temperature (°C)	25 $\pm 2$	-25 $\pm 2$	25 $\pm 2$	85 $\pm 2$	25 $\pm 2$
	Char.	Capacitance Change																				
B (Y5P)	$\pm 10\%$																					
E (Y5U)	+22/-56%																					
F (Y5V)	+22/-82%																					
Step	1	2	3	4	5																	
Temperature (°C)	25 $\pm 2$	-25 $\pm 2$	25 $\pm 2$	85 $\pm 2$	25 $\pm 2$																	
Solderability of Leads	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	<p>The lead wire of a capacitor should be dipped into molten solder for <math>2 \pm 0.5</math> sec. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead</p> <p>Temp. of solder :</p> <p>Lead Free Solder (Sn-2Ag-0.5Cu) <math>250 \pm 5^\circ\text{C}</math>.</p>																				

Item		Specification	Testing Method
Soldering Effect (Non-Preheat)	Appearance	No marked defect	<p>As shown in figure, the lead wires should be immersed in solder of 260±5°C up to 1.5 to 2.0mm from the root of terminal for 3.5±0.5 sec.</p> <p>Pre-treatment : Capacitor should be stored at 85±2°C for 1 hr., and then placed at room condition for 24±2 hrs. before initial measurements.</p> <p>Post-treatment : Capacitor should be stored for 1 to 2 hrs. at room condition.</p>
	Capacitance Change	Within ±10%.	
	I.R.	≥1000MΩ	
	Dielectric Strength	Per Item 6	
Soldering Effect (On-Preheat)	Appearance	No marked defect	<p>First the capacitor should be stored at 120+0/-5°C for 60+0/-5 sec. Then, as in figure (see Item 9), the lead wires should be immersed in solder of 260+0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 sec.</p> <p>Pre-treatment and Post-treatment see Per Item 9.</p>
	Capacitance Change	Within ±10%	
	I.R.	≥ 1000MΩ	
	Dielectric Strength	Per Item 6	
Vibration Resistance	Appearance	No marked defect	<p>The capacitor should be firmly soldered to the supporting lead wire and vibrated at a frequency range of 10 to 55Hz, 1.5mm in total amplitude, with about a 1 minute rate of vibration change from 10Hz to 55Hz and back to 10Hz. Apply for a total of 6 hrs., 2 hrs each in 3 mutually perpendicular directions.</p>
	Capacitance	Within the specified tolerance	
	D.F.	B(Y5P), E(Y5U), F(Y5V) : D.F. ≤ 2.5%	
Humidity (Under Steady State)	Appearance	No marked defect	<p>Set the capacitor for 500±12 hrs. at 40±2°C in 90 to 95% relative humidity.</p> <p>Post-treatment : Capacitor should be stored for 1 to 2 hrs. at room condition.</p>
	Capacitance Change	B(Y5P), E(Y5U) · F(Y5V) : ≤ ±15%	
	I.R.	>3000MΩ	
	Dielectric Strength	Per Item 6	



Item		Specification	Testing Method					
Humidity Loading	Appearance	No marked defect	Apply the rated voltage for 500±12 hrs. at 40±2°C in 90 to 95% relative humidity. Post-treatment : Capacitor should be stored for 1 to 2 hrs. at room condition.					
	Capacitance Change	B(Y5P), E(Y5U) · F(Y5V) : ≤ ±15%						
	I.R.	>3000MΩ						
	Dielectric Strength	Per Item 6						
Life Test	Appearance	No marked defect	Impulse Voltage : Each individual capacitor should be subjected to a 5kV (Type X1Y1: 8kVDC) impulses for three times. After the capacitors are applied to life test. 					
	Capacitance Change	Within ±20%						
	I.R.	>3000MΩ						
	Dielectric Strength	Per Item 6						
			Apply a voltage of Table 4 for 1000 hrs. at 125+2/-0°C, and relative humidity of 50% max. < Table 4 > <table border="1" style="width: 100%; text-align: center;"> <thead> <tr> <th>Applied Voltage</th> </tr> </thead> <tbody> <tr> <td>AC425V(r.m.s.), except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 sec.</td> </tr> </tbody> </table> Post-treatment : Capacitor should be stored for 1 to 2 hrs. at room condition.	Applied Voltage	AC425V(r.m.s.), except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 sec.			
Applied Voltage								
AC425V(r.m.s.), except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 sec.								
Flame Test	The capacitor flame discontinues as follows. <table border="1" style="width: 100%; text-align: center;"> <thead> <tr> <th>Cycle</th> <th>Time (sec.)</th> </tr> </thead> <tbody> <tr> <td>1 ~ 4</td> <td>30</td> </tr> <tr> <td>5</td> <td>60</td> </tr> </tbody> </table>	Cycle	Time (sec.)	1 ~ 4	30	5	60	The capacitor should be subjected to applied flame for 15 sec. and then removed for 15 sec. until 5 cycles are completed. 
Cycle	Time (sec.)							
1 ~ 4	30							
5	60							

Item		Specification	Testing Method																		
Robustness of Terminations	Tensile	Lead wire should not be cut off. Capacitor should not be broken.	As shown in the figure at right, fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec. 																		
	Bending		Each lead wire should be subjected to 5N weight and then a 90° bend, at the point of egress, in one direction, return to original position, and then apply a 90° bend in the opposite direction at the rate of one bend in 2 to 3 sec.																		
Temperature rapid change	Appearance	No marked defect	The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th colspan="3">Temperature Cycle</th> </tr> <tr> <th>Step</th> <th>Temperature (°C)</th> <th>Time (min)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-25+0/-3</td> <td>30</td> </tr> <tr> <td>2</td> <td>Room temp.</td> <td>3</td> </tr> <tr> <td>3</td> <td>125+3/-0</td> <td>30</td> </tr> <tr> <td>4</td> <td>Room temp.</td> <td>3</td> </tr> </tbody> </table>	Temperature Cycle			Step	Temperature (°C)	Time (min)	1	-25+0/-3	30	2	Room temp.	3	3	125+3/-0	30	4	Room temp.	3
	Temperature Cycle																				
	Step	Temperature (°C)		Time (min)																	
	1	-25+0/-3		30																	
	2	Room temp.		3																	
3	125+3/-0	30																			
4	Room temp.	3																			
Capacitance Change	B(Y5P), E(Y5U) : ±20% F(Y5V) : ±30%																				
D.F.	B(Y5P), E(Y5U) : D.F.≤ 5.0% F(Y5V) : D.F.≤ 7.5%																				
I.R.	>3000MΩ																				
Dielectric Strength	Per Item 6																				

Item	Specification	Testing Method
Active Flammability	The cheese-cloth should not be on fire.	<p>The capacitor should be individually wrapped in at least one but not more than two complete layers of cheese-cloth. The capacitor should be subjected to 20 discharges. The interval between successive discharges should be 5 sec. The UAC should be maintained for 2 min. after the last discharge.</p>  <p> C1, 2:1UF±10% °  C3:0.033UF±5% · 10KV °  Ct:3UF±5% 10KV °  Cx: Capacitor under test °  F: Fuse, Rated 10A  R:100Ω±5% °  Ur: Rated Voltage  Ut: Voltage applied to Ct  L1 to 4: 15mH±20% 16A Rod core choke </p> 